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## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re the pplication of:

Tai H. Nguyen, et al.

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Filed:

Serial No:

October 30, 2001

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Art Unit: 2183

TI-30112

Examiner:

Shared Component Clock Protection for Multicore DSP Device

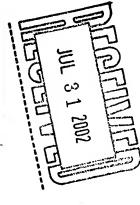
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Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231 on July 22, 2002.

Robin E. Barnum



Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,

Dobert of Marshell

Robert D. Marshall, Jr. Attorney for Applicants

Reg. No. 28,527

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Approved for use through 10/31/2002. OMB 0651-0031

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	STATEMENT			First Named Inventor	Tai H. Nguyen, et al.		
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Sheet	1	of	3 2007	Attorney Docket No.	TI-30112		
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		U.S. Patent Document		Name of Pate 1051	Date of Pub. of			
Exam. Initials*	Cite No.1	Number	Kind Code <sup>2</sup> (if known)	Applicant of Cited Doc.	Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	AA	5,677,849A		Stephen A. Smith	10/14/1997	KFCEIVED		
	AB	5,710,881A		Alan H. Karp, et al.	1/20/1998	TO LIVED		
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	FOREIGN PATENT DOCUMENTS								
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Other Prior Art/Non-Patent Literature Documents: <sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English Translation is attached.

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